

10/562004

IAP20 Rec'd PCT/PTO 23 DEC 2005
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q92367

Paul A. CAIN, et al

Appln. No.: National Stage of PCT/GB2004/002879

Confirmation No.: Unknown

Group Art Unit: Unknown

Filed: December 23, 2005

Examiner: Unknown

For: RECTIFYING DIODES

INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the documents which are listed on the attached PTO/SB/08 A & B (modified) form and/or listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

One copy of each of the listed documents is submitted herewith, except for the following: U.S. patents and/or U.S. patent publications; and co-pending non-provisional U.S. applications filed after June 30, 2003.

The present Information Disclosure Statement is being filed: (1) No later than three months from the application's filing date; (2) Before the mailing date of the first Office Action on the merits (whichever is later); or (3) Before the mailing date of the first Office Action after

10/562004

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filing a request for continued examination (RCE) under §1.114, and therefore, no Statement under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(p) is required.

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant encloses herewith a copy of a Communication from a foreign patent office in a counterpart application citing such documents (International Search Report for PCT/GB2004/002879 dated February 14, 2005), together with an English-language version (if not already included) of at least that portion of the Communication indicating the degree of relevance found by the foreign patent office.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this paper is attached.

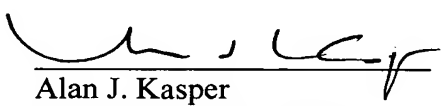
Respectfully submitted,

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE

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CUSTOMER NUMBER


Alan J. Kasper
Registration No. 25,426

Date: December 23, 2005

Substitute for Form 1449 A & B/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if Known

Application Number	National Stage of PCT/JP2004/028701 10/562004
Confirmation Number	Unknown
Filing Date	December 23, 2005
First Named Inventor	Paul A. CAIN
Art Unit	Unknown
Examiner Name	
Attorney Docket Number	Q92367

Sheet

1

of

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
		US			
		US			
		US			
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)			
		WO	02/095805	A2	11/28/2002	PLASTIC LOGIC LIMITED	

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
		H. L. GOMES, ET AL, "SCHOTTKY BARRIER DIODES FROM SEMICONDUCTING POLYMERS", IEE PROCEEDINGS: CIRCUITS DEVICES AND SYSTEMS, INSTITUTION OF ELECTRICAL ENGINEERS, STENVENAGE, GB, Vol. 144, No. 2, April 9, 1997, pages 117-122, XP006008326, ISSN: 1350-2409	
		L. S. ROMAN, ET AL, "POLYMER DIODES WITH HIGH RECTIFICATION", APPLIED PHYSICS LETTERS, AMERICAN INSTITUTE OF PHYSICS. NEW YORK, US, Vol. 75, No. 22, November 29, 1999, pages 3557-3559, XP012024179, ISSN: 0003-6951	
		K. M. STROHM, ET AL, "PLANAR 100 GHz SILICON DETECTOR CIRCUITS", MICROELECTRONIC ENGINEERING, ELSEVIER PUBLISHERS BY., AMSTERDAM, NL, Vol. 15, No. 1/4, October 1, 1991, pages 285-288, XP000292768, ISSN: 0167-9317	
		BEN RIVERA, ET AL, "DESIGN AND LAYOUT OF SCHOTTKY DIODES IN A STANDARD CMOS PROCESS", 2001 INTERNATIONAL SEMICONDUCTOR DEVICE RESEARCH SYMPOSIUM. SYMPOSIUM PROCEEDINGS (CAT. NO. 01EX497) IEEE, December 5, 2001, pages 79-82, XP002296313, PISCATAWAY, NJ, USA, ISSN: 0-7803-7432-0	

Examiner Signature

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to indicate here if English language Translation is attached.